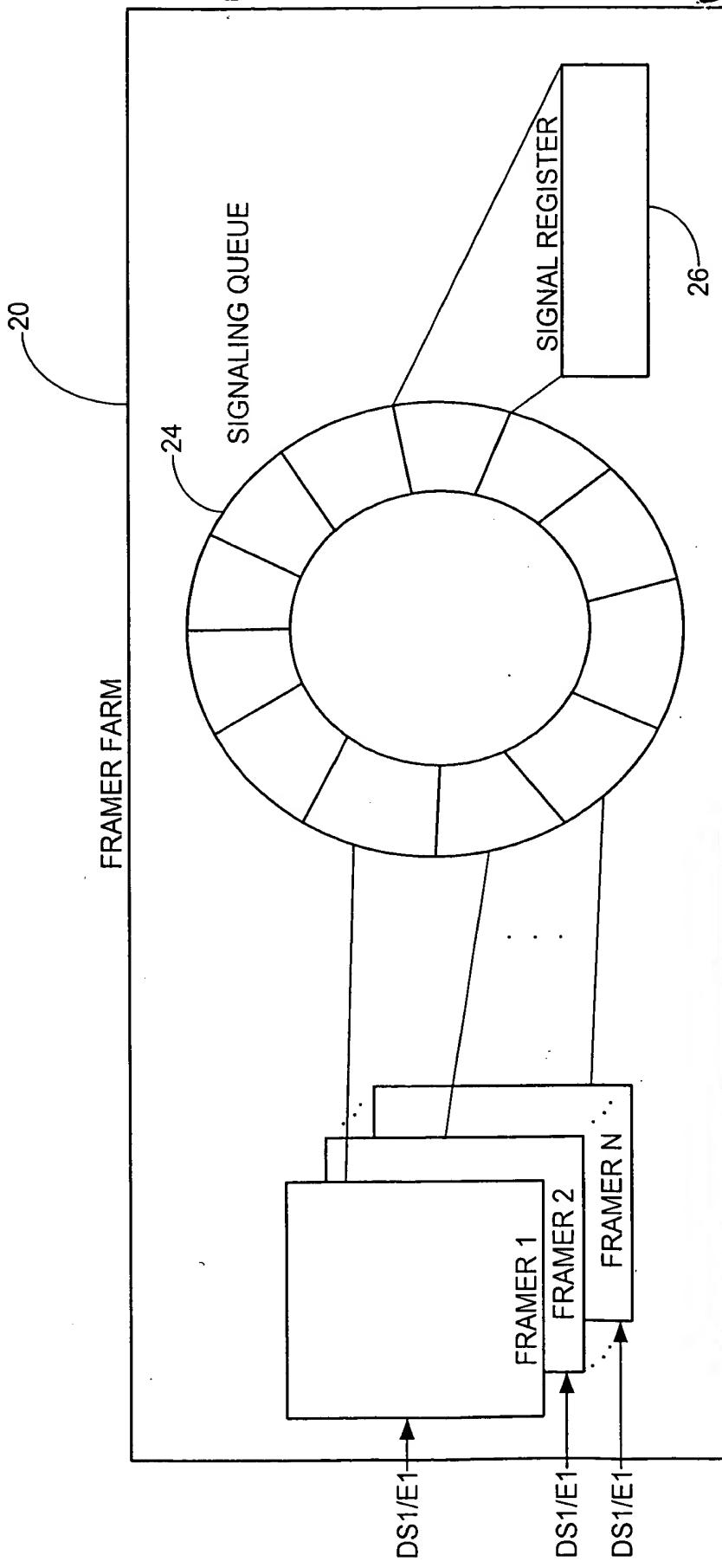
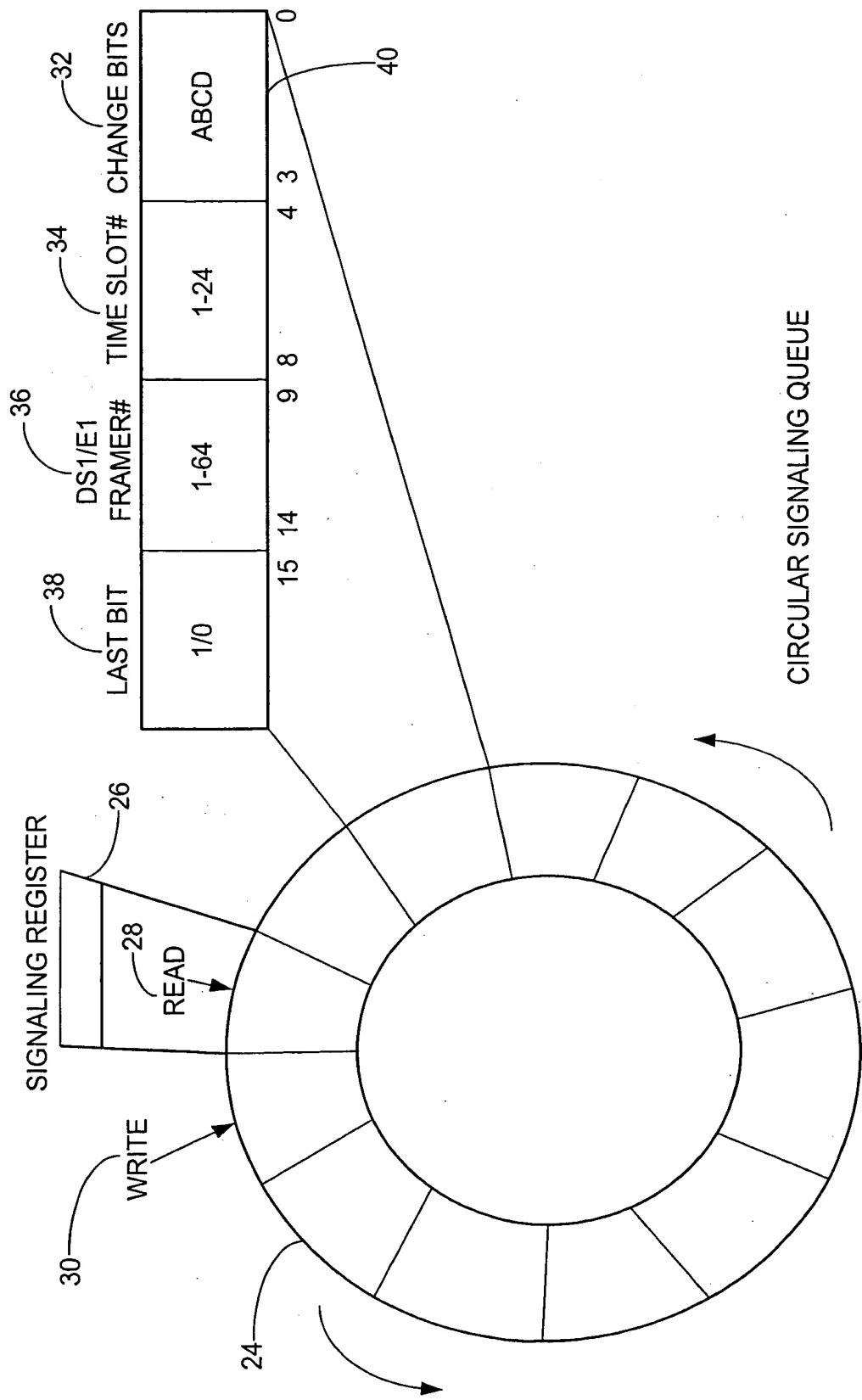


**FIG. 1**



FRAMER FARM WITH N DS1/E1 FRAMER

**FIG. 2**



**FIG. 3**

CIRCULAR SIGNALING QUEUE

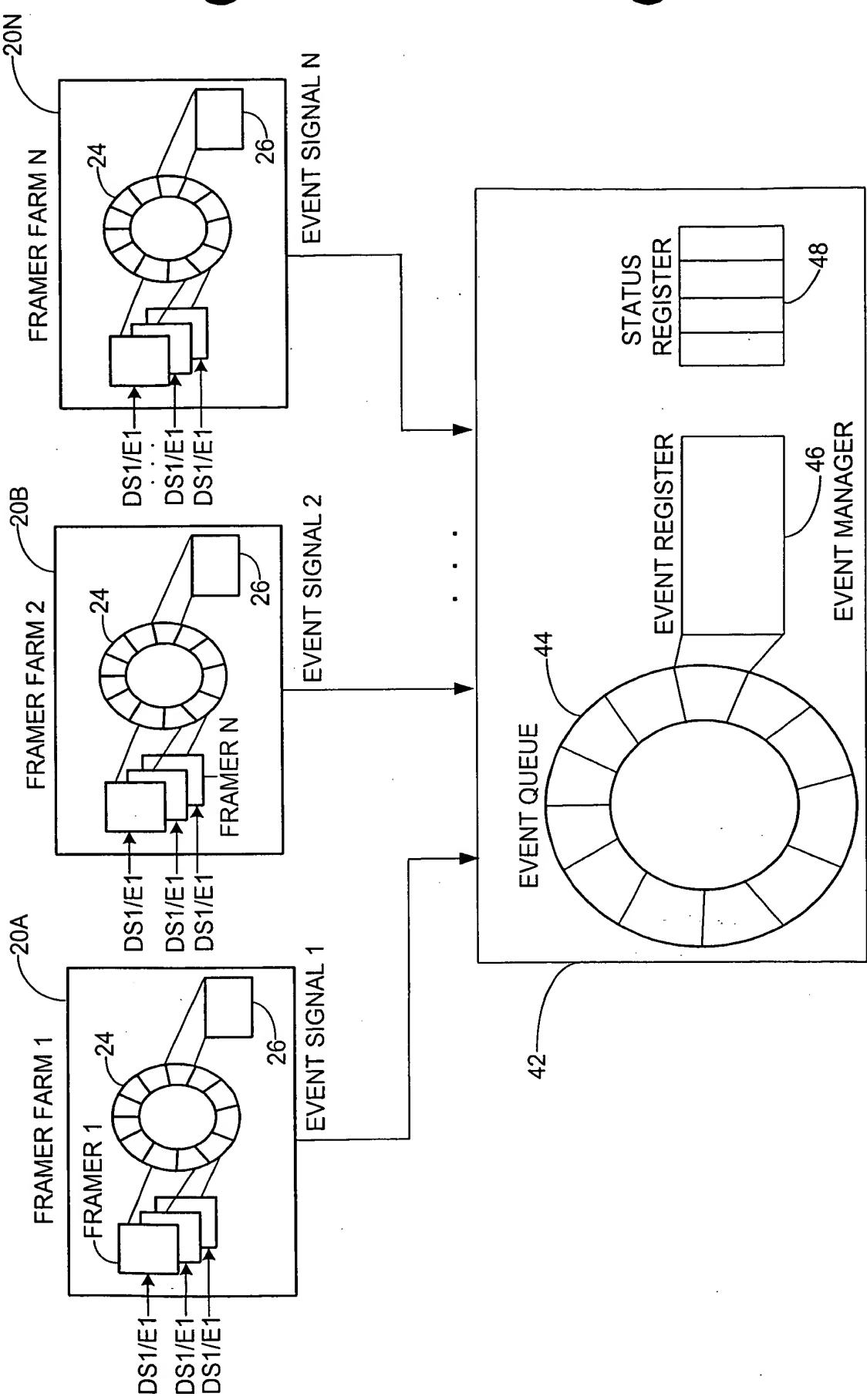


FIG. 4

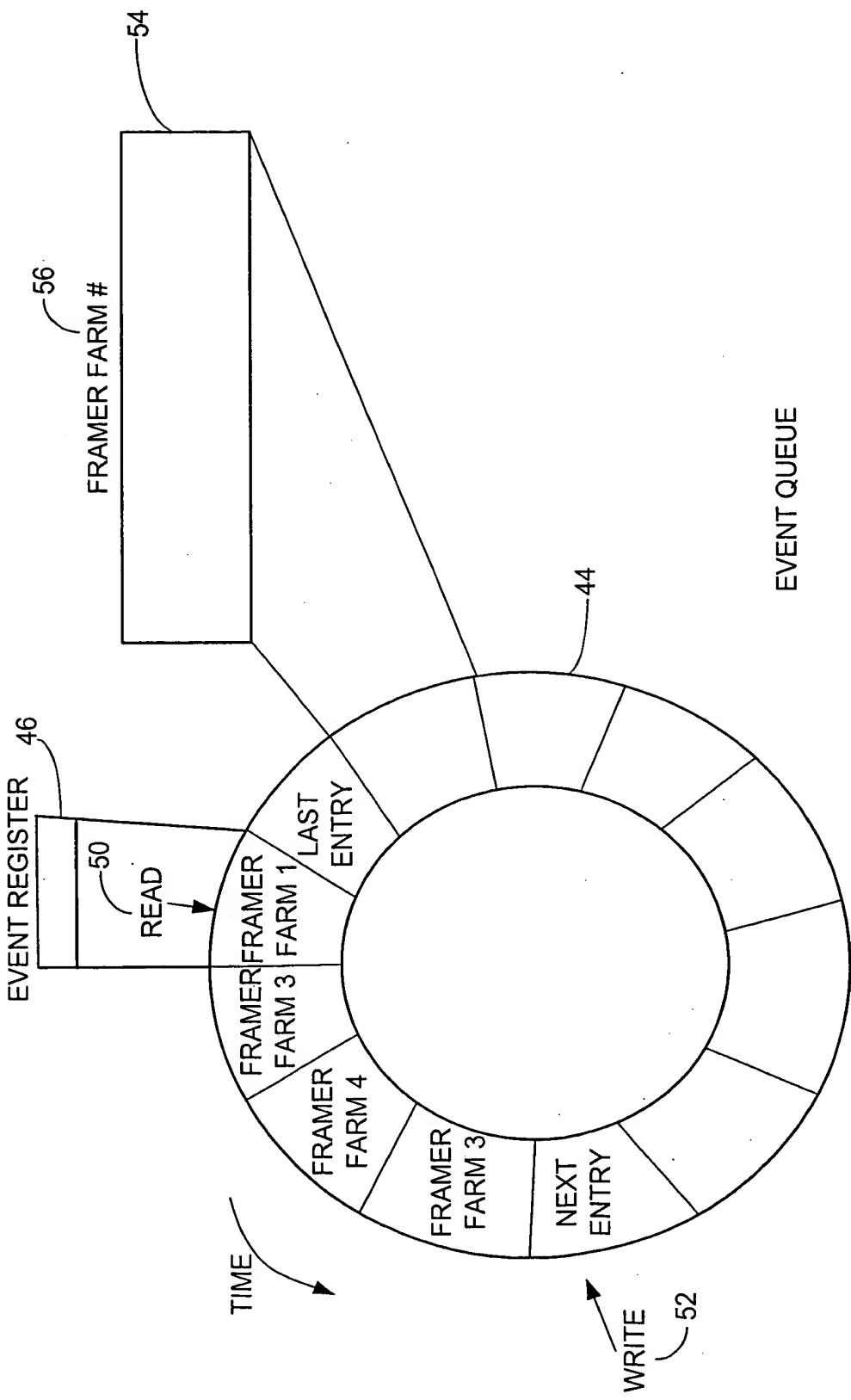


FIG. 5A

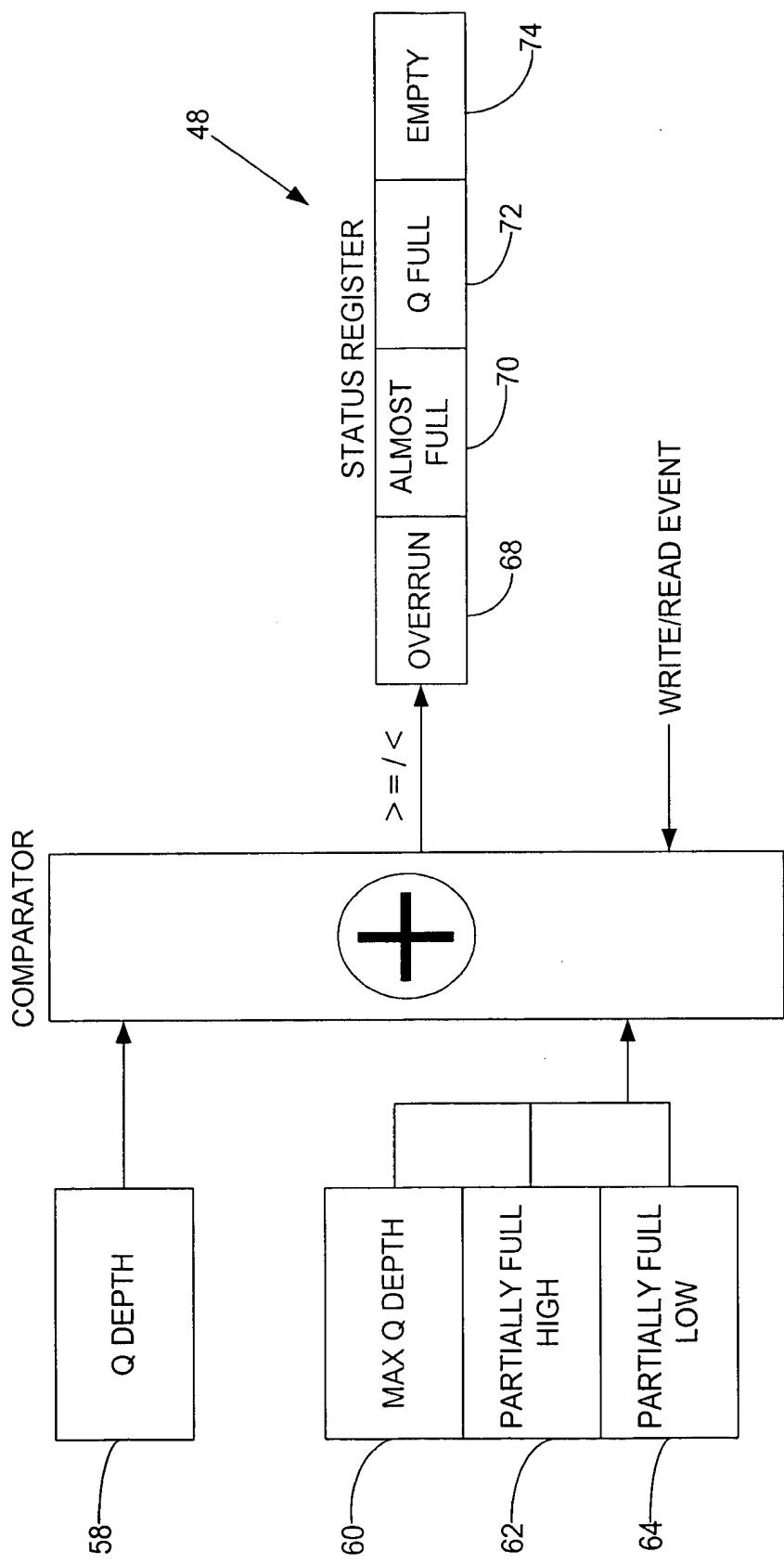
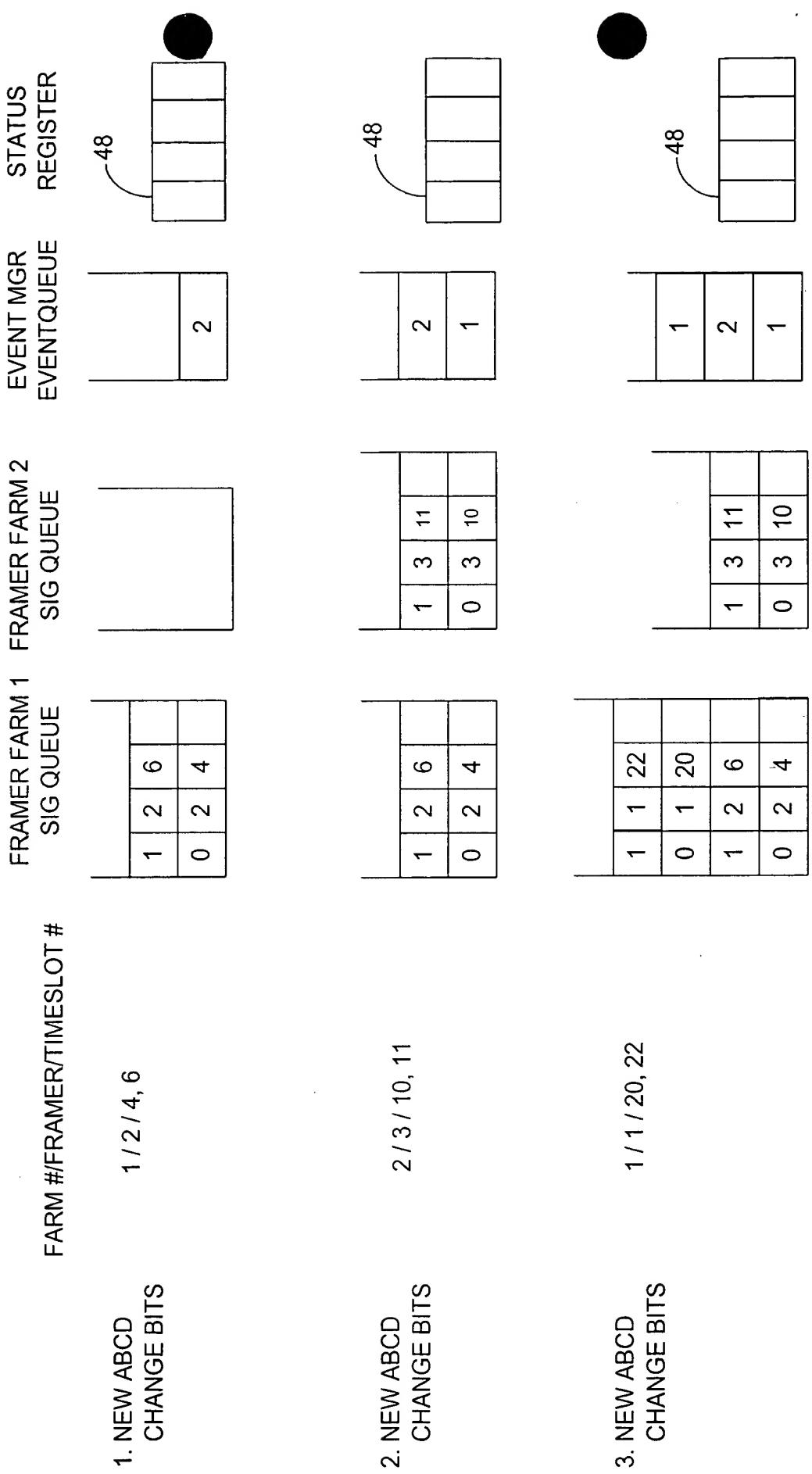
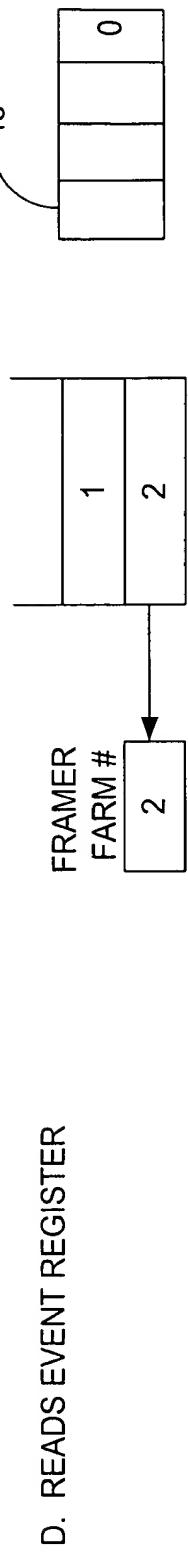
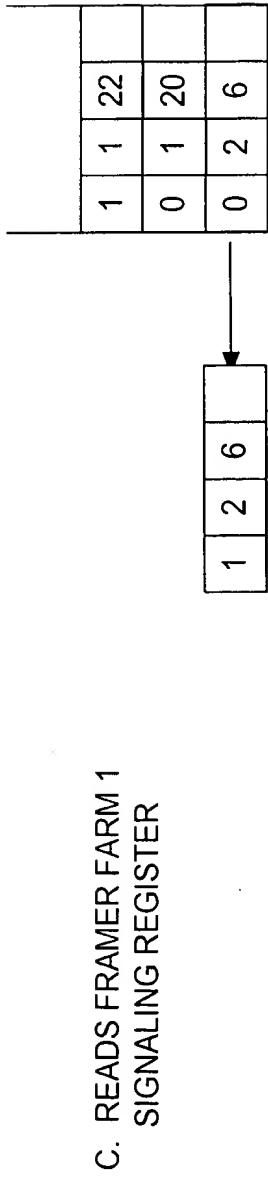
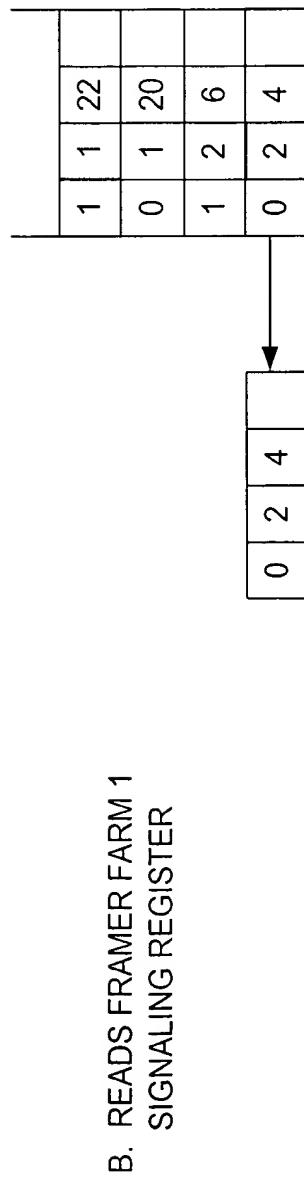
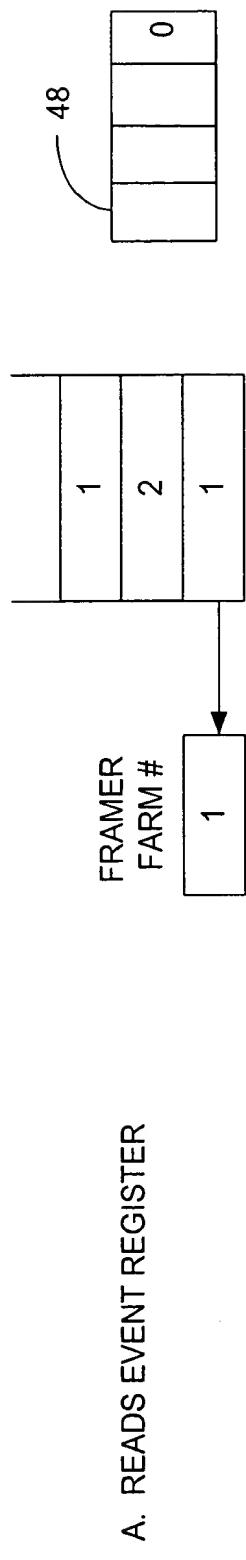


FIG. 5B



#### 4. MICROPROCESSOR PROCESSES EVENTS

STATUS  
REGISTER



**FIG. 6B**

#### 4. MICROPROCESSOR PROCESSES EVENTS

STATUS  
REGISTER

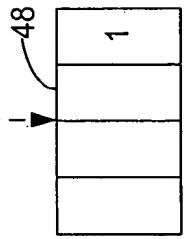
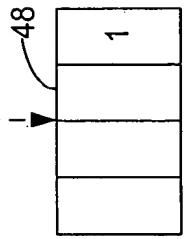
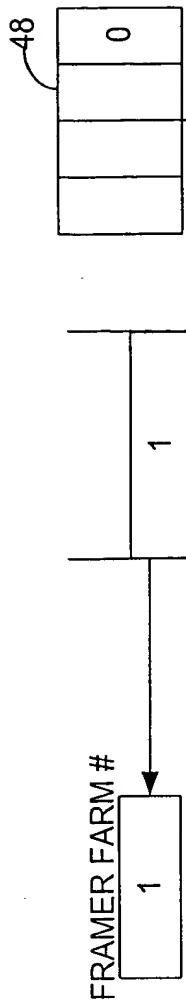
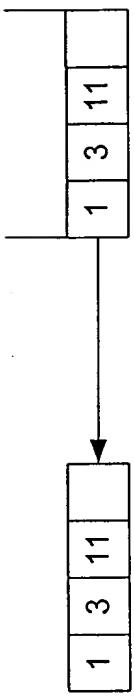
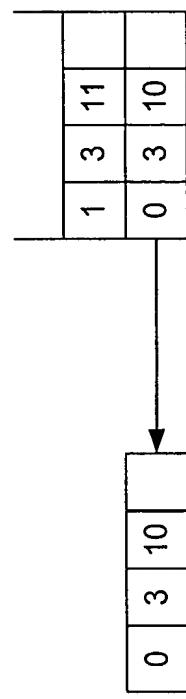
E. READS FRAMER FARM 2  
SIGNALING REGISTER

F. READS FRAMER FARM 2  
SIGNALING REGISTER

G. READS EVENT REGISTER

H. READS FRAMER FARM 1  
SIGNALING REGISTER

I. READS FRAMER FARM 1  
SIGNALING REGISTER



**FIG. 6C**

**FIG. 7**

1 / 2 / 4  
1 / 2 / 6  
2 / 3 / 10  
2 / 3 / 11  
1 / 1 / 20  
1 / 1 / 22

FRAMER FARM 1  
SIGNALING QUEUE

1

1	2	6	
0	2	4	

FRAMER FARM 2  
SIGNALING QUEUE

--	--	--	--

2

1	2	6	
0	2	4	

1	3	11	
0	3	10	

3

1	1	22	
0	1	20	
0	2	6	
0	2	4	

1	3	11	
0	3	10	

FIG. 8

PROCESSOR

1 / 2 / 4

1 / 2 / 6

1 / 1 / 20

1 / 1 / 22

2 / 3 / 10

2 / 3 / 11

FIG. 9